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## AVR220: Bubble Sort

### Features

- 14-word Subroutine Sorts up to 255 Bytes of Data
- Runnable Demo Program

### Introduction

This application note implements the Bubble Sort algorithm on the AVR controllers. The subroutine “bubble” sorts up to 224 bytes of SRAM data which is the SRAM area that can be reached by the lower eight bits of a pointer.

### The Bubble Sort Algorithm – Theory

The Bubble Sort algorithm is known as a quite slow and trivial algorithm for data sorting. However, for small amounts of data, the algorithm provides compact code and relatively fast sorting.

Given an array of data 1, 2, ...,  $n-1$ ,  $n$ , the algorithm is described as follows:

1. Compare elements  $n-1$  and  $n$ .
2. If  $n-1$  is lower than  $n$ , swap the contents of the two array locations.
3. Repeat steps 1 and 2 for elements  $n-2$  and  $n-1$ . Move up one location at a time, repeat until elements 1 and 2 have been compared and possibly swapped.
4. Repeat the whole run from element  $n$  to 2.
5. Repeat the run from element  $n$  to 3.
6. ...
7. Compare and, if needed, swap elements  $(n-1)$  and  $n$ .
8. When completed, the array is sorted with the highest value in location 0 and the lowest one in location  $n$ .

While the algorithm is executed, the higher elements move (“bubble”) through the array until they reach their final position. After the first run, the highest value finds its final position. After the second run, the second highest value finds its final position, and so on...

The total number of compare operations needed to sort an array of  $n$  elements is:

$$\sum_{i=0}^{n-1} i = \frac{n(n-1)}{2}$$

As the total compare time grows exponentially, with the number of elements to sort, calculate the execution time first if before sorting a large number of bytes.



8-bit **AVR**<sup>®</sup>  
Microcontroller

Application  
Note

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In Pseudo-code, the Bubble Sort algorithm would be as follows:

```
for i=n downto 1 do
begin
for j=n downto i
begin
if A(n-1)<A(n)
swap (A(n-1),A(n))
end
end
end
```

To reverse the sort order, replace the “<” sign with “>”.

## Implementation

### Usage

The subroutine “bubble” is used according to the following procedure:

1. Load “endH:endL” with the address of last element in the array.
2. Load the Loop counter “cnt1” with the size of the data array - 1.
3. Call “bubble”.

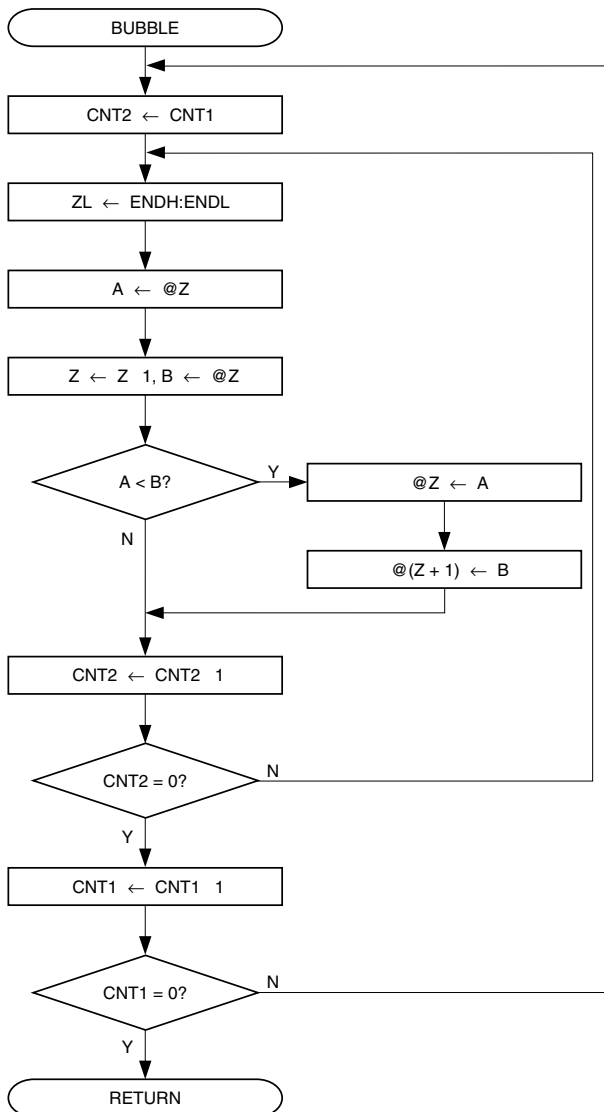
If preferred, the routine will work fine using the Y-pointer instead.

### Algorithm Description

The following procedure describes how the sorter is implemented on the AVR:

1. Copy “cnt1” to “cnt2”.
2. Copy “endH:endL” to “Z”.
3. Load register variable “A” with the byte at Z.
4. Decrement Z and load register variable “B” with the byte at Z.
5. If  $A < B$ , store “A” at Z and “B” at Z+1 (swap bytes).
6. Decrement “cnt2”.
7. If not zero, goto Step 2.
8. Decrement “cnt1”.
9. If not zero, goto Step 1.

Figure 1. “bubble” Flow Chart



Performance

Table 1. “bubble” Register Usage

Register	Input	Internal	Output
R13		“A” – First Value to Compare	
R14		“B” – Second Value to Compare	
R15		“cnt2” – Inner Loop Counter	
R16	“cnt1” – # of Bytes to sort - 1	“cnt1” – Outer Loop Counter	
R17	“endL” – Low Address of Last Element		
R18	“endH” – High Address of Last Element		
R30		ZL	
R31		ZH	

**Table 2.** “bubble” Performance Figures <sup>(1)</sup>

Parameter	Value
Code Size (Words)	12 + return
Average Execution Time (Cycles)	$5 \times (\text{SIZE}-1) + 11.5 \times (\text{SIZE}(\text{SIZE}-1)) + \text{return}$
Register Usage	<ul style="list-style-type: none"> <li>• Low Registers :None</li> <li>• High Registers :2</li> <li>• Pointers :Z</li> </ul>
Interrupts Usage	None
Peripherals Usage	None

Note: 1. SIZE = Number of bytes to sort

## Test/Example Program

“avr220.asm” contains a test program which copies 60 bytes of random data from the Program memory to SRAM and calls “bubble” to sort the data. The test program is well suited for running under the AVR Studio<sup>®</sup>. To get a feeling for how the data “bubbles” through the array, place data a Break Point somewhere in the inner loop and run single loop cycles while watching the SRAM memory window.



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